

# **Up to 6 GHz Low Noise Silicon Bipolar Transistor**

## Technical Data

#### AT-41410

## **Features**

- Low Noise Figure: 1.6 dB Typical at 2.0 GHz 3.0 dB Typical at 4.0 GHz
- High Associated Gain: 14.0 dB Typical at 2.0 GHz 10.0 dB Typical at 4.0 GHz
- High Gain-Bandwidth Product: 8.0 GHz Typical f<sub>T</sub>
- Hermetic, Gold-ceramic Microstrip Package

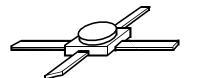
## **Description**

Hewlett-Packard's AT-41410 is a general purpose NPN bipolar transistor that offers excellent high frequency performance. The AT-41410 is housed in a hermetic, high reliability 100 mil ceramic package. The 4 micron emitter-to-emitter pitch enables this transistor to be used in many different functions. The 14 emitter finger

interdigitated geometry yields an intermediate sized transistor with impedances that are easy to match for low noise and moderate power applications. This device is designed for use in low noise, wideband amplifier, mixer and oscillator applications in the VHF, UHF, and microwave frequencies. An optimum noise match near 50  $\,\Omega$  at 1 GHz , makes this device easy to use as a low noise amplifier.

The AT-41410 bipolar transistor is fabricated using Hewlett-Packard's 10 GHz  $f_T$  Self-Aligned-Transistor (SAT) process. The die is nitride passivated for surface protection. Excellent device uniformity, performance and reliability are produced by the use of ionimplantation, self-alignment techniques, and gold metalization in the fabrication of this device.

100 mil Package



5965-8923E 4-104

**AT-41410 Absolute Maximum Ratings** 

Symbol	Parameter	Units	Absolute Maximum <sup>[1]</sup>
$V_{\mathrm{EBO}}$	Emitter-Base Voltage	V	1.5
$V_{\mathrm{CBO}}$	Collector-Base Voltage	V	20
$V_{CEO}$	Collector-Emitter Voltage	V	12
$I_{\mathrm{C}}$	Collector Current	mA	60
$P_{T}$	Power Dissipation [2,3]	mW	500
$T_{\rm j}$	Junction Temperature	°C	200
$T_{STG}$	Storage Temperature	°C	-65 to 200

Thermal Resistance $[2,4]$ :	
$\theta_{\rm jc} = 170^{\circ} \text{C/W}$	

#### **Notes:**

- 1. Permanent damage may occur if any of these limits are exceeded.
- 2.  $T_{CASE} = 25$ °C.
- 3. Derate at 5.9 mW/°C for  $T_C > 115$ °C.
- 4. The small spot size of this technique results in a higher, though more accurate determination of  $\theta_{jc}$  than do alternate methods. See MEASUREMENTS section "Thermal Resistance" for more information.

## Electrical Specifications, $T_A = 25$ °C

Symbol	Parameters and Test Conditions		Units	Min.	Тур.	Max.
$ S_{21E} ^2$	Insertion Power Gain; $V_{CE} = 8 \text{ V}$ , $I_{C} = 25 \text{ mA}$	$f = 2.0 \mathrm{GHz}$ $f = 4.0 \mathrm{GHz}$	dB		12.0 6.5	
P <sub>1 dB</sub>	Power Output @ 1 dB Gain Compression $V_{CE} = 8  V, I_C = 25  mA$	$f = 2.0 \mathrm{GHz}$ $f = 4.0 \mathrm{GHz}$	dBm		19.0 18.5	
$G_{1 dB}$	1 dB Compressed Gain; $V_{CE} = 8 \text{ V}, I_{C} = 25 \text{ mA}$	$f = 2.0 \mathrm{GHz}$ $f = 4.0 \mathrm{GHz}$	dB		14.0 9.5	
NF <sub>O</sub>	Optimum Noise Figure: $V_{CE} = 8 \text{ V}, I_{C} = 10 \text{ mA}$	f = 1.0 GHz f = 2.0 GHz f = 4.0 GHz	dB		1.3 1.6 3.0	1.9
$G_A$	Gain @ NF <sub>O</sub> ; $V_{CE} = 8 \text{ V}$ , $I_{C} = 10 \text{ mA}$	$\begin{aligned} &f=1.0\mathrm{GHz}\\ &f=2.0\mathrm{GHz}\\ &f=4.0\mathrm{GHz} \end{aligned}$	dB	13.0	18.5 14.0 10.0	
$f_{\mathrm{T}}$	Gain Bandwidth Product: $V_{CE} = 8 \text{ V}, I_{C} = 25 \text{ mA}$		GHz		8.0	
h <sub>FE</sub>	Forward Current Transfer Ratio; $V_{CE} = 8 \text{ V}, I_{C} = 10 \text{ mA}$		_	30	150	270
$I_{CBO}$	Collector Cutoff Current; $V_{CB} = 8 \text{ V}$		μA			0.2
$I_{EBO}$	Emitter Cutoff Current; $V_{EB} = 1 V$		μΑ			1.0
$C_{CB}$	Collector Base Capacitance <sup>[1]</sup> : $V_{CB} = 8 \text{ V}, f = 1 \text{ MHz}$		pF		0.2	

## **Notes:**

1. For this test, the emitter is grounded.

## AT-41410 Typical Performance, $T_A = 25$ °C

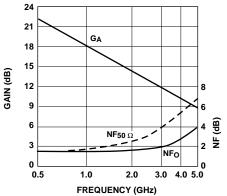


Figure 1. Noise Figure and Associated Gain vs. Frequency.  $V_{\rm CE}$  = 8 V,  $I_{\rm C}$  = 10mA.

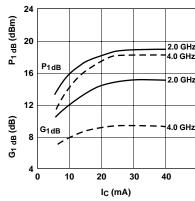


Figure 2. Output Power and 1 dB Compressed Gain vs. Collector Current and Frequency.  $V_{CE} = 8 \text{ V}.$ 

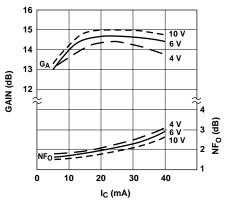


Figure 3. Optimum Noise Figure and Associated Gain vs. Collector Current and Collector Voltage. f = 2.0 GHz.

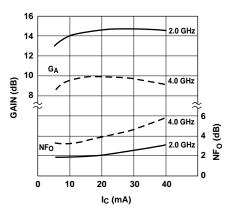


Figure 4. Optimum Noise Figure and Associated Gain vs. Collector Current and Frequency.  $V_{CE}$  = 8 V.

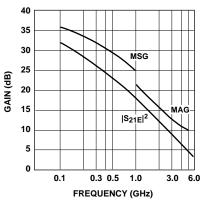


Figure 5. Insertion Power Gain, Maximum Available Gain and Maximum Stable Gain vs. Frequency.  $V_{\rm CE}=8~V,~I_{\rm C}=25~mA.$ 

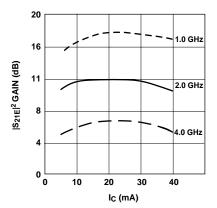


Figure 6. Insertion Power Gain vs. Collector Current and Frequency.  $V_{\rm CE}$  = 8 V.

AT-41410 Typical Scattering Parameters, Common Emitter,  $Z_O = 50~\Omega$ ,  $T_A = 25^{\circ}C$ ,  $V_{CE} = 8~V$ ,  $I_C = 10~mA$ 

Freq.	;	$S_{11}$		$\mathbf{S}_{21}$			$\mathbf{S}_{12}$		S	22
GHz	Mag.	Ang.	dB	Mag.	Ang.	dB	Mag.	Ang.	Mag.	Ang.
0.1	.61	-40	27.7	24.38	159	-40.0	.010	75	.94	-13
0.5	.60	-127	22.2	12.83	110	-30.4	.030	40	.62	-33
1.0	.60	-163	17.1	7.12	86	-28.2	.039	35	.50	-38
1.5	.60	179	13.8	4.89	71	-27.5	.042	45	.46	<b>-4</b> 2
2.0	.61	165	11.4	3.72	59	-26.0	.050	42	.45	-48
2.5	.61	157	9.7	3.04	52	-24.7	.058	46	.44	-52
3.0	.62	149	8.2	2.56	42	-23.9	.064	50	.44	-58
3.5	.63	140	7.0	2.23	31	-22.3	.077	48	.46	-68
4.0	.62	130	5.9	1.96	20	-21.3	.086	44	.48	-78
4.5	.61	120	4.9	1.76	10	-20.4	.095	41	.50	<b>-</b> 85
5.0	.61	106	4.0	1.59	-1	-18.9	.113	38	.52	<b>-</b> 91
5.5	.62	94	3.2	1.45	-11	-18.3	.121	33	.52	-97
6.0	.66	82	2.4	1.31	-22	-17.5	.133	30	.51	-105

AT-41410 Typical Scattering Parameters, Common Emitter,  $Z_O=50~\Omega, T_A=25^{\circ}C, V_{CE}=8~V, I_C=25~mA$ 

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Freq.		$S_{11}$		$\mathbf{S}_{21}$			$\mathbf{S_{12}}$		S	22
GHz	Mag.	Ang.	dB	Mag.	Ang.	dB	Mag.	Ang.	Mag.	Ang.
0.1	.45	-69	31.4	37.17	150	-39.2	.011	64	.87	-18
0.5	.58	-153	23.3	14.63	101	-33.6	.021	43	.49	-33
1.0	.59	-178	17.7	7.68	81	-30.4	.030	53	.43	-35
1.5	.60	169	14.3	5.21	68	-28.2	.039	58	.41	<b>-4</b> 0
2.0	.60	157	11.9	3.94	56	-25.8	.051	55	.41	<b>-4</b> 5
2.5	.61	151	10.1	3.20	50	-24.4	.060	55	.40	<b>-4</b> 9
3.0	.62	144	8.6	2.70	40	-23.1	.070	58	.40	-56
3.5	.63	135	7.4	2.35	30	-21.9	.080	54	.42	-66
4.0	.62	126	6.3	2.07	19	-20.5	.094	53	.44	-76
4.5	.61	116	5.3	1.85	9	-19.3	.108	45	.46	-84
5.0	.61	103	4.5	1.67	<b>-</b> 2	-18.5	.119	41	.49	-90
5.5	.63	91	3.6	1.52	-12	-17.6	.131	34	.49	-96
6.0	.67	80	2.8	1.37	<b>-</b> 22	-16.8	.144	29	.47	-104

A model for this device is available in the DEVICE MODELS section.

AT-41410 Noise Parameters:  $V_{CE} = 8 \text{ V}, I_{C} = 10 \text{ mA}$ 

Freq.	NFo	Γ	R <sub>N</sub> /50		
GHz	dB	Mag	Ang	It <sub>N</sub> /30	
0.1	1.2	.12	4	0.17	
0.5	1.2	.10	23	0.17	
1.0	1.3	.06	49	0.16	
2.0	1.6	.26	172	0.16	
4.0	3.0	.46	-133	0.26	

## 100 mil Package Dimensions

